EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3794	375/316,318,340,345.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/25 16:47
L2	1836	455/130,150.1,194.1,194.2,234.1, 239.1,240.1,241.1,245.1,250.1.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/25 16:50
S1	140	(register) same (gain near2 control$4) near4 ((variable near2 gain near2 amplifier) or ("vga"))	US-PGPUB; USPAT	OR	ON	2006/10/23 11:04
S2	14	(register) near2 (gain near2 control$4) near4 ((variable near2 gain near2 amplifier) or ("vga"))	US-PGPUB; USPAT	OR	ON	2006/10/23 11:13
S3	233	(register) near2 ((variable near2 gain near2 amplifier) or ("vga"))	US-PGPUB; USPAT	OR	ON	2006/10/23 11:13
S4	77414	gain near3 control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 11:13
S5	39	S3 and S4	US-PGPUB; USPAT	OR	ON	2006/10/23 11:14
S6	36	S5 not S2	US-PGPUB; USPAT	OR	ON	2006/10/23 11:22
S7	16913	(gain near2 control$4)near2 (circuit or device)	US-PGPUB; USPAT	OR	ON	2006/10/23 11:22
S8	11790	variable near2 gain near2 amplifier	US-PGPUB; USPAT	OR	ON	2006/10/23 11:41
S9	2875	S7 and S8	US-PGPUB; USPAT	OR	ON	2006/10/23 11:23
S10	389610	register	US-PGPUB; USPAT	OR	ON	2006/10/23 11:23
S11	766	S9 and S10	US-PGPUB; USPAT	OR	ON	2006/10/23 11:23
S12	77482	time near2 division	US-PGPUB; USPAT	OR	ON	2006/10/23 11:23
S13	174	S11 and S12	US-PGPUB; USPAT	OR	ON	2006/10/23 11:23
S14	183542	hold$3 near3 (circuit or device)	US-PGPUB; USPAT	OR	ON	2006/10/23 11:24
S15	24	S13 and S14	US-PGPUB; USPAT	OR	ON	2006/10/23 11:27
S16	868	375/345.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/23 11:28
S17	314887	(register or buffer or memory) near3 (control$4)	US-PGPUB; USPAT	OR	ON	2006/10/23 11:29
S18	108	S16 and S17	US-PGPUB; USPAT	OR	ON	2006/10/23 11:34

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S19	5382	(control$4 near3 gain) with ((variable near2 gain near2 amplifier) or ("vga"))	US-PGPUB; USPAT	OR	ON	2006/10/23 11:36
S20	72959	rece$5 near2 frame	US-PGPUB; USPAT	OR	ON	2006/10/23 11:35
S21	224	S19 and S20	US-PGPUB; USPAT	OR	ON	2006/10/23 11:35
S22	210	S21 not S15	US-PGPUB; USPAT	OR	ON	2006/10/23 11:35
S23	118	S12 and S22	US-PGPUB; USPAT	OR	ON	2006/10/23 11:41
S24	27	((variable near1 gain near1 amplifier) or ("vga")) near2 gain near2 control$4 near4 (register)	US-PGPUB; USPAT	OR	ON	2006/10/23 12:01
S25	4220	((variable near1 gain near1 amplifier) or ("vga")) with gain near2 control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 12:01
S26	3215	((variable near1 gain near1 amplifier) or ("vga")) near2 gain near2 control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 12:01
S27	41	((variable near1 gain near1 amplifier) or ("vga")) near2 gain near2 control$4 near3 (register or buffer or memory)	US-PGPUB; USPAT	OR	ON	2006/10/23 12:01
S28	963242	frame	US-PGPUB; USPAT	OR	ON	2006/10/23 12:02
S29	8	S27 and S28	US-PGPUB; USPAT	OR	ON	2006/10/23 12:04
S30	33	S27 not S29	US-PGPUB; USPAT	OR	ON	2006/10/23 12:18
S31	80	(hold$3) near3 (register or buffer or memory) same (level near3 (detect$3) with (circuit or device))	US-PGPUB; USPAT	OR	ON	2006/10/23 12:20
S32	4	(hold$3) near3 (register or buffer or memory) same (level near3 (detect$3) with (circuit or device)) same gain near3 control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 12:20
S33	22	(period) with generat$3 same time with measur$4 same gain near3 control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 16:17
S39	439	(register or buffer or memory or "f/f" or stor$3) with (initial or first) near3 value same gain with control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 16:50

S40	315	(register or buffer or memory or "f/f" or stor$3) with (initial or first) near3 value same gain near4 control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 16:50
S41	216	(register or buffer or memory or "f/f" or stor$3) near4 (initial or first) near3 value same gain near4 control$4	US-PGPUB; USPAT	OR	ON	2006/10/23 16:51
S42	419907	amplifier	US-PGPUB; USPAT	OR	ON	2006/10/23 16:51
S43	154	S41 and S42	US-PGPUB; USPAT	OR	ON	2006/10/23 16:51
S44	681	(first or "1st") near2 (level near2 detect$3) with (circuit or device) same (second or "2nd" or other) near2 (level near2 detect$3) with (circuit or device)	US-PGPUB; USPAT	OR	ON	2006/10/23 17:23
S45	10	(first or "1st") near2 (level near2 detect$3) with (circuit or device) same (second or "2nd" or other) near2 (level near2 detect$3) with (circuit or device) same (compar$4) same (gain near3 control$4)	US-PGPUB; USPAT	OR	ON	2006/10/23 17:33
S46	0	IKUO-YASUI.in.	US-PGPUB; USPAT	OR	ON	2006/10/23 17:33
S47	4	YASUI-IKUO.in.	US-PGPUB; USPAT	OR	ON	2006/10/23 17:35
S48	11	SATO-HISAYASU.in.	US-PGPUB; USPAT	OR	ON	2006/10/23 17:34
S49	1766	(stor$3) near3 (gain near3 (value or amount))	US-PGPUB; USPAT	OR	ON	2006/10/24 11:21
S50	220	(stor$3) near3 (gain near3 (value or amount)) with register	US-PGPUB; USPAT	OR	ON	2006/10/24 11:22
S51	26973	(variable near2 amplifier) or "vga"	US-PGPUB; USPAT	OR	ON	2006/10/24 11:33
S52	70	S50 and S51	US-PGPUB; USPAT	OR	ON	2006/10/24 11:32
S53	588	(control$4 or estimat$3 or calculat$3 or evaluat$3) near3 gain near4 (stor$3) near4 (register or memory or buffer)	US-PGPUB; USPAT	OR	ON	2006/10/24 11:33
S54	16816	(variable near1 amplifier) or "vga"	US-PGPUB; USPAT	OR	ON	2006/10/24 11:33
S55	57	S53 and S54	US-PGPUB; USPAT	OR	ON	2006/10/24 11:34

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S56	1	(header near3 information) same (period near3 generat$3) same (gain near3 control$4)	US-PGPUB; USPAT	OR	ON	2006/10/25 10:16
S57	0	(header near3 information) same (time near3 measur$4) same (gain gain near3 control$4)	US-PGPUB; USPAT	OR	ON	2006/10/25 10:16
S58	278	(header near3 information) same (gain gain near3 control$4)	US-PGPUB; USPAT	OR	ON	2006/10/25 10:16
S59	75188	period near3 (generat$3 or measur$4)	US-PGPUB; USPAT	OR	ON	2006/10/25 10:17
S60	11	S58 and S59	US-PGPUB; USPAT	OR	ON	2006/10/25 16:42

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = YASUI
First Name = IKUO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06941215	Not Issued	161	12/12/1986	BINARY ADDER CIRCUIT WITH BUFFERED SKIP-AHEAD CARRY	YASUI, IKUO
07113612	4870609	150	10/26/1987	HIGH SPEED FULL ADDER USING COMPLEMENTARY INPUT-OUTPUT SIGNALS	YASUI, IKUO
07156397	Not Issued	166	02/16/1988	MICROPROCESSOR WITH HARVARD ARCHITECTURE	YASUI, IKUO
07475625	Not Issued	161	01/30/1990	FULL ADDER USING COMPLEMENTARY INPUT AND OUTPUT, AND HAVING SENSING AMPLIFIER AT OUTPUT PART THEREOF	YASUI, IKUO
07560246	5034887	150	07/27/1990	MICROPROCESSOR WITH HARVARD ARCHITECTURE	YASUI, IKUO
07610179	5162667	150	11/07/1990	SEMICONDUCTOR INTEGRATED CIRCUIT WITH MASTER AND SLAVE LATCHES	YASUI, IKUO
08780921	Not Issued	161	01/09/1997	SPEECH ENCODER/DECODER WITH SPEECH RECORDING AND PRODUCTION FUNCTIONS	YASUI, IKUO
10665468	Not Issued	30	09/22/2003	Reception apparatus for receiving time-division signal	YASUI, IKUO

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
YASUI	IKUO	Search

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Inventor Name Search Result

Your Search was:

Last Name = SATO
First Name = HISAYASU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06918977	Not Issued	166	10/15/1986	METHOD OF PRODUCING A MASTER SLICE SYSTEM LARGE SIZED SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	SATO, HISAYASU
07212029	Not Issued	161	06/23/1988	METHOD OF PRODUCING A MASTER SLICE SYSTEM LARGE SIZED SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	SATO, HISAYASU
08058063	5406136	250	05/10/1993	A HIGH SPEED ECL OUTPUT CIRCUIT HAVING REDUCED POWER CONSUMPTION	SATO, HISAYASU
08247973	Not Issued	161	05/23/1994	BIPOLAR LOGIC CIRCUIT	SATO, HISAYASU
09350916	6144233	150	07/12/1999	GAIN CONTROL CIRCUIT FOR CONTROLLING OPERATION OF A VARIABLE-GAIN AMPLIFIER OR THE LIKE	SATO, HISAYASU
09547920	6826393	150	04/11/2000	MIXER CIRCUIT HAVING COMPONENT FOR FREQUENCY CONVERSION	SATO, HISAYASU
09900017	6798678	150	07/09/2001	FREQUENCY VOLTAGE CONVERTER	SATO, HISAYASU
10193300	6639446	150	07/12/2002	HIGH LINEARITY, HIGH GAIN MIXER CIRCUIT	SATO, HISAYASU
10644865	Not Issued	161	08/21/2003	Oscillator circuit and L load differential circuit achieving a wide oscillation frequency range and low phase noise characteristics	SATO, HISAYASU
10665468	Not Issued	30	09/22/2003	Reception apparatus for receiving time-division signal	SATO, HISAYASU
11083242	Not Issued	30	03/18/2005	Frequency mixer preventing degradation in linearity on amplitude of input signal	SATO, HISAYASU
11280410	Not Issued	30	11/17/2005	Oscillator circuit and L load differential circuit achieving a wide oscillation frequency range and low phase noise characteristics	SATO, HISAYASU

08399289	5602498	150	03/06/1995	CURRENT SWITCHING LOGIC TYPE CIRCUIT WITH SMALL CURRENT CONSUMPTION	SATOH, HISAYASU
08735834	5754062	150	10/23/1996	CURRENT SWITCHING LOGIC TYPE CIRCUIT WITH SMALL CURRENT CONSUMPTION	SATOH, HISAYASU
08788837	6472925	150	01/27/1997	MIXER CIRCUIT WITH NEGATIVE FEED BACK FILTERING	SATOH, HISAYASU
08982032	5973539	150	12/01/1997	MIXER CIRCUIT FOR MIXING TWO SIGNALS HAVING MUTUALLY DIFFERENT FREQUENCIES	SATOH, HISAYASU
09090240	6100760	150	06/04/1998	VARIABLE GAIN AMPLIFIER	SATOH, HISAYASU
09173618	6127892	150	10/16/1998	AMPLIFICATION CIRCUIT	SATOH, HISAYASU
09632269	6313704	150	08/03/2000	Multi-stage signal amplifying circuit	SATOH, HISAYASU
09961184	6522711	150	09/24/2001	VARIABLE FREQUENCY DIVIDER CIRCUIT	SATOH, HISAYASU
09974856	6522007	150	10/12/2001	SEMICONDUCTOR DEVICE HAVING DUMMY PATTERNS FOR METAL CMP	SATOH, HISAYASU
10187622	6717469	150	07/03/2002	GAIN CONTROL CIRCUIT FOR CONTROLLING A GAIN IN A VARIABLE GAIN CELL	SATOH, HISAYASU
10193150	6642540	150	07/12/2002	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	SATOH, HISAYASU
10309272	6784548	150	12/04/2002	SEMICONDUCTOR DEVICE HAVING DUMMY PATTERNS FOR METAL CMP	SATOH, HISAYASU
10351483	6686861	150	01/27/2003	SLICE CIRCUIT CAPABLE OF ACCURATE CONVERSION OF AN ANALOG SIGNAL TO A DIGITAL SIGNAL	SATOH, HISAYASU
10383568	6791413	150	03/10/2003	VARIABLE GAIN AMPLIFIER WITH A GAIN EXHIBITING A LINEAR IN DB CHARACTERISTIC RELATIVE TO A CONTROL VOLTAGE	SATOH, HISAYASU

Inventor Search Completed: No Records to Display.

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	SATO	HISAYASU	Search

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